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Dynamic Nonlinear Resistance Model for a Power MOSFET in an Oscillatory RLC Circuit

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Abstract—This paper presents the development of a new MATLAB MOSFET model specifically designed for RLC circuits. The key contribution is the formulation of a novel equation that accurately captures the device behavior across subthreshold, above-threshold regions and at threshold point, addressing limitations in existing models. The developed model treats the MOSFET as a variable resistance element, with the resistance changing dynamically at each instant, enabling the solution of differential equations governing the RLC circuit. Curve fitting and refinement were conducted based on experimental results, leading to a close match between the simulations and experimental data. The model was tested with triangle, sinusoidal and quadrilateral gate voltages, and the simulation results show good match with the experimental data, demonstrating the model's accuracy. It provides a straightforward way to predict performance, making it easier to refine and optimize the design gate voltage before physical implementation. This work provides a solid foundation for MOSFET modeling in oscillatory RLC circuits, which can be applied to a wide range of power electronics applications.

Index Terms—Supercapacitors, transcranial magnetic stimulation (TMS), power semiconductor switches, pulse generator, magnetic stimulation, pulse circuits, nonlinear MOSFET model.

I. INTRODUCTION

METAL-OXIDE semiconductor field effect transistor (MOSFET) models play a crucial role in simulating and understanding device behavior in power electronics. Various MOSFET models have been developed to accurately represent their characteristics in circuit simulations. These models fall into three broad categories: physics-based, analytical, and behavioral. Physics-based models, often used in technology computer-aided design (TCAD) simulations, focus on detailed internal phenomena such as electron mobility, channel length modulation, and threshold voltage shifts. While these models offer high accuracy, they can be computationally intensive and difficult to use for practical applications in power circuits due to the complexity of the physical phenomena being modeled [1], [2]. Analytical models, such as the Unified Charge Control Model (UCCM), rely on mathematical expressions to describe device behavior and to provide a clear relationship between key parameters like current, transconductance, and threshold voltage. Though

more efficient for simulations than physics-based models, they can become complicated when high accuracy is required [3], [4].

Behavioral models, including widely-used SPICE models, are designed to simplify the simulation of MOSFETs for practical applications like power converters, automotive systems, and switching devices. These models focus on representing the overall electrical behavior of the MOSFET rather than the internal physical details. SPICE models are generally preferred for applications requiring fast and efficient simulation because they strike a balance between accuracy and simplicity. A SPICE model for a 6.5 kV SiC MOSFET was recently developed to closely match the device's static and dynamic characteristics, demonstrating how behavioral models can still achieve reasonable accuracy for power applications [5]. While behavioral models are simpler and more user-friendly, they can lack the precision needed for complex scenarios, particularly where parasitic elements (stray inductance and capacitance) or extreme operating conditions such as high switching frequencies or elevated temperatures come into play.

One of the major challenges in MOSFET modeling is the accurate simulation of switching behavior and parasitic effects. SiC MOSFETs offer lower on-state resistance than traditional silicon MOSFET, higher breakdown voltages, and faster switching speeds, making them suitable for high-voltage and high-frequency applications [6]. While several models have been proposed for SiC MOSFETs, such as the Hefner model and its derivatives [7], these models often require extensive parameter extraction from datasheets or experimental data, limiting their practical use in everyday circuit design. A recent approach involves using a modified Enz-Krumenacher-Vittoz (EKV) model [8] for SiC MOSFETs, which offers a single equation for the entire operating range, from weak to strong inversion. This model has been successfully applied to simulate SiC MOSFETs in high-frequency switching applications, with validation against experimental data.

Several studies have focused on modeling SiC power devices. PSpice is widely used for SiC MOSFET modeling, but its limitations in handling complex equations can affect accuracy [8]. MATLAB/Simulink, with superior simulation and data processing capabilities, offers an alternative. Tornello et al. [9] developed an analytical model for single and parallel SiC configurations, achieving accurate

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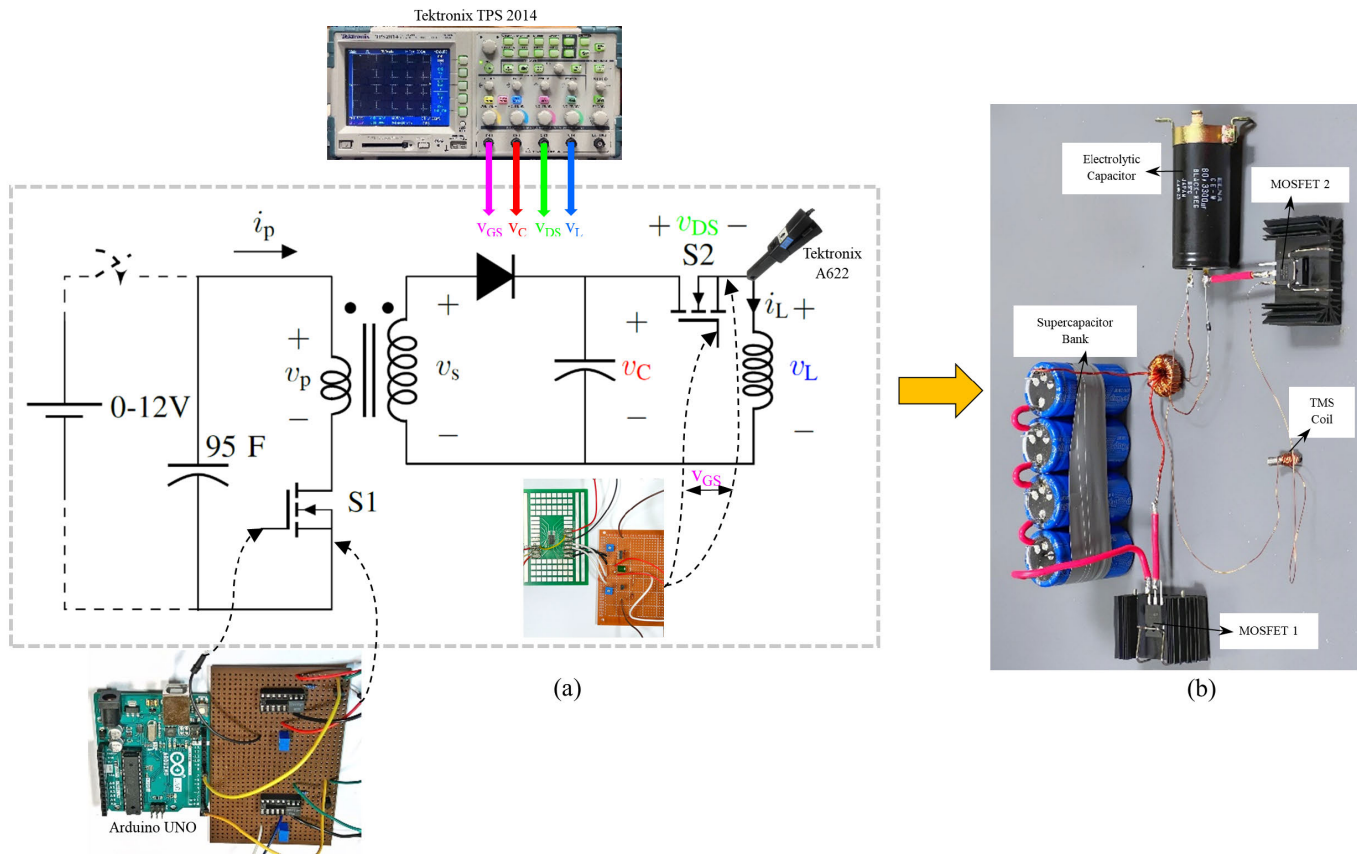


Fig. 1. Proof-of-concept prototype. Gate control signals for MOSFET S1 and S2 ensure that these switches are never ON at the same time. (a) Overall system with photos inserted showing external drivers to S1 from Arduino processor board, gate-drive waveform generator for S2 and oscilloscope; (b) experimental power stage.

88 switching transient behavior and energy loss predictions
 89 compared to SPICE models using 2D and 1D Look-Up
 90 Tables (LUTs) for device characteristics. Ferretti et al. [10]
 91 implemented a MATLAB-based approach to create I-V-T maps
 92 (lookup tables), including operating regions not covered in
 93 datasheets. Zeng et al. [11] conducted modeling research
 94 on parasitic capacitances (C_{ds}) and (C_{gd}) using MATLAB
 95 Simulink, extracting values from datasheets and curve-fitting
 96 for accuracy. While these studies relied on datasheet-based
 97 modeling and simulation, none explored the internal MOSFET
 98 variations, such as dynamic resistance changes, highlighting a
 99 gap in the current research.

100 Wang et al. [12] proposed a modeling method using PSpice
 101 with a dual voltage-dependent lookup table approach based on
 102 the envelope of the switching trajectory. Their method defines
 103 the value ranges for gate-source voltage (v_{GS}) and drain-source
 104 voltage (v_{DS}) during interterminal capacitance operation
 105 periods, considering only essential data within the switching
 106 trajectory and discarding unnecessary information outside it.
 107 Talesara et al. [13] implemented an analytical subcircuit model
 108 for SiC MOSFETs into Synopsys Saber [14], validating it
 109 against measured DC characteristics. They employed distinct
 110 equations for sub-threshold and threshold regions instead
 111 of a unified equation and used a Simulink-based approach
 112 rather than a purely mathematical one. Yang et al. [15]
 113 employ small-signal oscillations, utilizing linear analysis

without attempting to model the actual switching phase
 transition.

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This paper aims to address this gap by proposing a new
 MOSFET model, developed in MATLAB, which is based
 on experimental results and tailored for use in electrical
 RLC circuits as first trial step. The model introduces a
 new equation that simplifies the representation of MOSFET
 behavior, focusing on the static and dynamic characteristics
 that are critical for power electronics applications. Unlike
 existing models that are either overly complex or limited in

140 their accuracy, this model is designed to be easy to implement
 141 while maintaining close alignment with experimental results.
 142 This approach offers a practical solution for engineers looking
 143 to simulate MOSFETs in power converters, motor controllers,
 144 and other high-voltage applications, where both accuracy and
 145 simplicity are essential. By bridging the gap between detailed
 146 physics-based models and simplified behavioral models, the
 147 proposed model provides a more balanced solution for modern
 148 power electronics design [5], [6], [8], [16], [17].

149 This paper details the development of a MATLAB simulation
 150 to model a MOSFET, enabling predictions of its behavior and
 151 the output of an RLC circuit. This RLC circuit is integral to
 152 the supercapacitor-based TMS pulse generator [18], which has
 153 been developed and successfully tested. To advance the design
 154 further, a simulation model was essential to verify output
 155 pulses, perform precise pulse shaping, and assess the impact of
 156 varying gate voltages on TMS pulse characteristics. However,
 157 no existing MOSFET equation or model was suitable for this
 158 application, prompting us to derive a new equation and create
 159 a MATLAB MOSFET model tailored for in-depth analysis.
 160 The process begins with an overview of the background
 161 study conducted for modeling, followed by modifications to
 162 existing equations to ensure proper functionality within the
 163 circuit. The creation of the MATLAB model is then described,
 164 based on experimental data, resulting in a complete MOSFET
 165 simulation model capable of predicting output current and
 166 voltage as functions of time, in response to gate voltage
 167 input. Finally, a comparison between the modeling and
 168 experimental results for a different scenario is presented.
 169 This study presents a solution applicable to any oscillatory
 170 RLC circuit incorporating a MOSFET, offering insights and
 171 modeling techniques beneficial for a range of applications
 172 beyond TMS pulse generation.

173 II. EXPRESSION FOR DRAIN CURRENT

174 Figure 1 shows the overall system, including the circuit
 175 diagram of the TMS pulse generator setup. This pulse
 176 generator [18] incorporates an RLC circuit in the final stage to
 177 excite the TMS coil. We developed this MOSFET model as part
 178 of our research study focused on pulse shaping capabilities and
 179 predicting the output TMS pulse. In this section, we discuss
 180 the modeling of the RLC part alone for a generic study.

181 Consider an RLC circuit containing an embedded MOSFET
 182 to control oscillatory current as shown in Fig. 2. We explicitly
 183 include the internal resistances of the capacitor (r_C) and the
 184 inductor (r_L).

185 Applying Kirchhoff's voltage law to Fig. 2,

$$186 \quad v_C = v_L + (r_L + r_C + r_{DS})i_L \quad (1)$$

187 where the ideal inductor voltage is given by $v_L = L \frac{di_L}{dt}$

188 Equation (1) can be rewritten as a first-order differential
 189 equation,

$$190 \quad \frac{di_L}{dt} = \frac{1}{L} [v_C - (r_L + r_C + r_{DS})i_L] \quad (2)$$

191 Thus if we know r_{DS} , we can numerically integrate Eq. (2) to
 192 find $i_L(t)$.

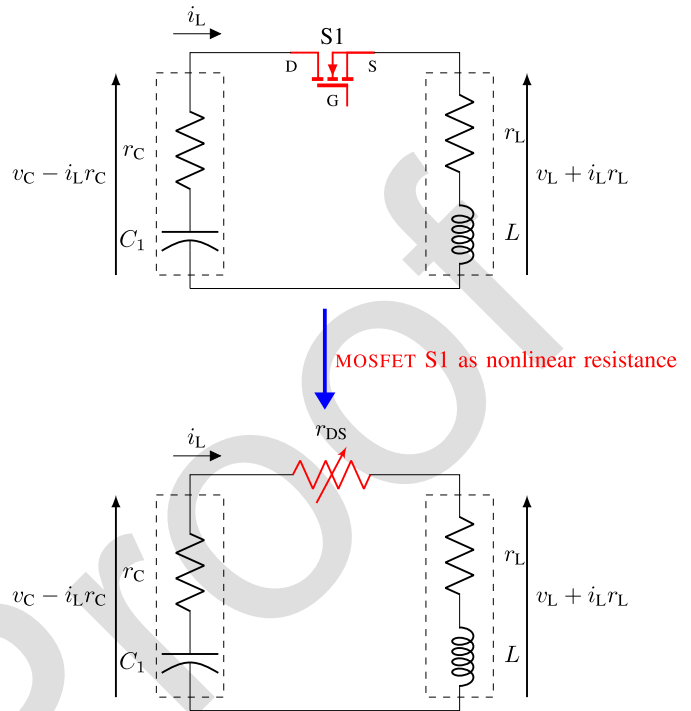


Fig. 2. The circuitry illustrates a RLC circuit, featuring the electrolytic capacitor C_1 along with its internal resistance r_C feeding an inductive load characterized by an inductance L and its internal resistance r_L . It includes a MOSFET switch represented as a nonlinear resistance r_{DS} , which depends on the gate-source voltage.

In these equations, we do not consider the MOSFET as a
 typical switching component. Instead, it is treated as an analog
 device, which behaves like a variable resistor controlled by the
 voltage applied to the gate. The challenge is to determine the
 time-varying resistance of the MOSFET as a function of input
 gate voltage for incorporation into differential equation (2).

By controlling the gate voltage v_{GS} , a MOSFET can be
 operated in distinct regions as shown in Fig. 3(a): subthreshold
 ($v_{GS} < v_T$), and above-threshold ($v_{GS} > v_T$). When gate
 voltage exceeds threshold, the MOSFET output characteristics
 moves from linear to saturation mode as v_{GS} increases. Our
 focus is on developing a unified description for MOSFET
 behavior that encompasses transition through threshold.

196 A. Subthreshold ($v_{GS} < v_T$)

In the subthreshold region, also known as the weak/cutoff
 region, the transistor remains in an 'off' state with minimal
 conduction. The small current flow into the drain is described
 by an exponential function of v_{GS} :

$$197 \quad i_D \approx i_{D0} \exp\left(\frac{v_{GS} - v_T}{\eta v_{Th}}\right) \quad (3) \quad 198$$

where applied gate voltage (v_{GS}), threshold voltage (v_T),
 thermal voltage (v_{Th}), and a subthreshold ideality factor (η) is
 used. The ideality factor accounts for the subthreshold division
 of the applied voltage between the gate insulator and the
 depletion layer [20].

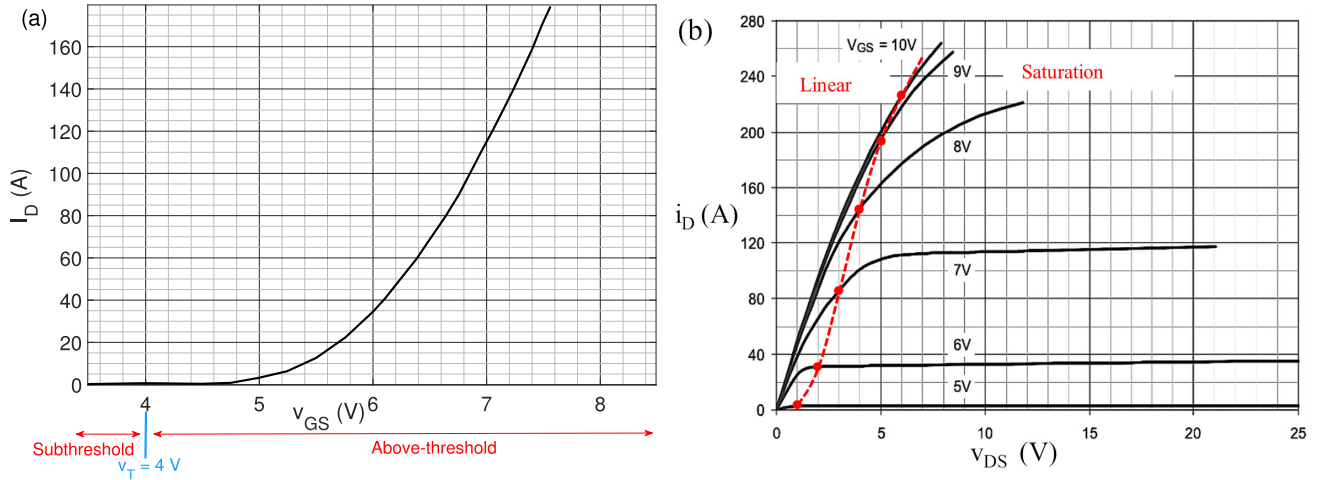


Fig. 3. (a) Drain current (I_D) against gate-source voltage (v_{GS}) of 25°C and (b) drain current against the drain-source voltage for various gate voltages for MOSFET IXFK120N65 \times 2 from data-sheet [19]. The red dotted line in (b) indicates the above-threshold separation between linear and saturation modes, corresponding to v_{DS} being less or greater than $(v_{GS} - v_T)$ respectively. The threshold voltage (v_T) is set to 4 V here for demonstration, as the datasheet specifies a range for v_T between 3 and 6 V.

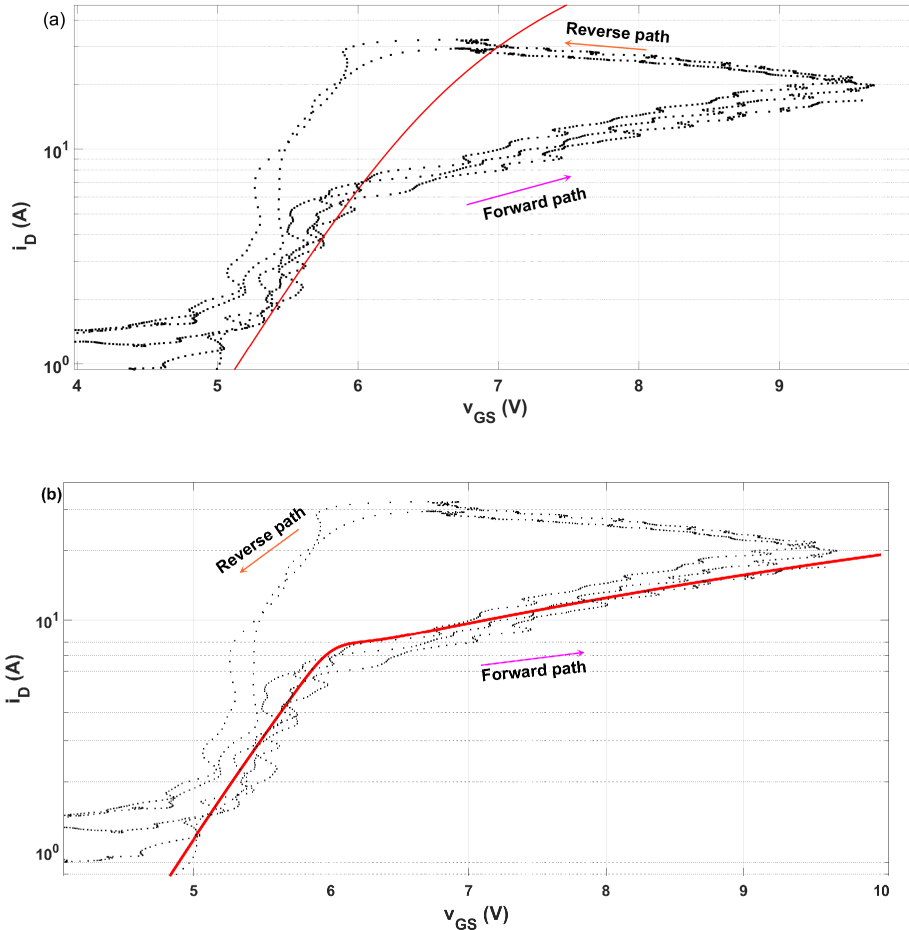


Fig. 4. (a) Curve fit (red line) for the MOSFET drain current i_L for data points (black dots) from experiment using Eq. (7) which failed to fit; (b) improved curve using equation Eq. (15), provided a good match for the forward-path measurements.

217 B. Above Threshold ($v_{GS} > v_T$)

218 In the above-threshold region, there are two operation modes
219 depending on the drain-current characteristic with respect to

drain-source voltage as in Fig 3(b). These are linear (also
referred to as triode or ohmic), and saturation modes. 220

- 1) Linear mode ($v_{GS} > v_T, v_{DS} < (v_{GS} - v_T)$), the
transistor activates, creating a narrow channel for current 222
223

to flow from drain to source. This region operates like a resistor, its conductance controlled by the gate-source voltage. The drain current in linear/ohmic mode consists of linear and quadratic terms as:

$$i_D = \mu_n C_{ox} \frac{W}{L} \left((v_{GS} - v_T) v_{DS} - \frac{v_{DS}^2}{2} \right) \quad (4)$$

where μ_n is the charge carrier effective mobility, C_{ox} is the gate oxide capacitance per unit area, gate width (W) and gate length (L) [21]. According to this equation, when the drain voltage is small, the drain current becomes a linear function of v_{DS} :

$$i_D = \mu_n C_{ox} \frac{W}{L} (v_{GS} - v_T) v_{DS}. \quad (5)$$

2) The saturation mode ($v_{GS} > v_T$, $v_{DS} > (v_{GS} - v_T)$), also above threshold, and since drain voltage is greater than the source voltage, it results in an increased electron dispersion and conduction through a broader channel. The drain current in saturation mode is:

$$i_D = \mu_n C_{ox} \frac{W}{L} [v_{GS} - v_T]^2 [1 + \lambda v_{DS}]. \quad (6)$$

The parameter λ accounts for the impact of channel length modulation on drain current, known as the Early effect, and it is measured in inverse volts (V^{-1}) [22]. It represents the change in drain current with respect to drain voltage due to variations in the channel length. Figure 3(b) shows the data-sheet version of input and output characteristics with the boundary between linear and saturation modes marked by the red line.

C. Near Threshold ($v_{GS} = v_T$)

In our application, we require the MOSFET to operate at and near threshold since the high voltage across the inductor coil is achieved at the time of switch off (when gate voltage goes below threshold). Surprisingly, this region has not been thoroughly analyzed by researchers [4], [23]. Analytical solutions like the parallel-plate charge-control model and the unified charge-control model exist [24], [25], [26], yet they lack precise analytical solutions for the subthreshold or near-threshold region. For many applications, an approximate solution used in these models is [4]:

$$i_D = 2 i_o \ln \left[1 + \frac{1}{2} \exp \left(\frac{v_{GS} - v_T}{\eta v_{Th}} \right) \right] \quad (7)$$

where i_o is the current corresponding to the minority carriers at threshold. This equation can be further expressed in terms of a variable X :

$$i_D = 2 i_o \ln \left[1 + \frac{X}{2} \right], \quad \text{where } X = \exp \left(\frac{v_{GS} - v_T}{\eta v_{Th}} \right). \quad (8)$$

Equation 7 can be evaluated across different regions to determine its applicability for sub-threshold, above-threshold, and near-threshold regions.

1) *Subthreshold Region*: In the subthreshold region ($v_{GS} < v_T$), where $X \ll 1$, we can approximate the exponential term for small values as:

$$\ln \left[1 + \frac{X}{2} \right] \approx \frac{X}{2} \quad \text{for small } X. \quad (9)$$

This simplifies Eq. (7):

$$i_D \approx 2 i_o \left[\frac{X}{2} \right] = i_o \exp \left(\frac{v_{GS} - v_T}{\eta v_{Th}} \right). \quad (10)$$

2) *Above-Threshold Region*: In the above-threshold region ($v_{GS} > v_T$), where $X \gg 1$, the exponential term dominates. We can approximate it as:

$$\ln \left[1 + \frac{X}{2} \right] \approx \ln \left[\frac{X}{2} \right]. \quad (11)$$

and Eq. (7) simplifies to:

$$i_D \approx 2 i_o \ln \left(\frac{1}{2} \right) + 2 i_o \frac{v_{GS} - v_T}{\eta v_{Th}}, \quad (12)$$

in agreement with Eq. (3).

3) *At Threshold*: At the threshold ($v_{GS} = v_T$), we have:

$$X = 1, \quad (13)$$

so the equation becomes:

$$i_D = 2 i_o \ln \left[1 + \frac{1}{2} \cdot 1 \right] = 2 i_o \ln \left(\frac{3}{2} \right). \quad (14)$$

Thus at the threshold condition, the current i_D is not equal to i_o .

Equation (7) is suitable for both above and below the threshold regions but not near the threshold region, as i_D is not equal to the initial current at the threshold point [27], [28], [29]. All analytical approaches available are designed for lower power and small-signal MOSFETs. But no analytical model have been developed for power MOSFETs. We initially tried to use Eq. (7) to fit the data points of the measured drain current versus gate voltage for our power MOSFET, but failed. Thus we modified the drain current equation to read:

$$i_D = \frac{i_o}{\ln 2} \ln \left(1 + \left[\exp \left(\frac{v_{GS} - v_T}{\eta v_{Th}} \right) \right]^p \right) \quad (15)$$

This equation can be further expressed in terms of a variable X :

$$i_D = \frac{i_o}{\ln 2} \ln [1 + X^p], \quad \text{where } X = \exp \left(\frac{v_{GS} - v_T}{\eta v_{Th}} \right). \quad (16)$$

Where, we have introduced a new parameter, the exponent p , and replaced the factor 2 in Eq. (7) with $\frac{1}{\ln 2}$ for reasons that will be explained in Section IV.

Figure 4(a) illustrates the failed curve fit using Eq. (7), while Fig. 4(b) demonstrates the successful fit achieved by employing the modified equation (15), which aligns much better with the data points (black dotted points).

III. METHODOLOGY

The following summarizes the steps we took to develop the MOSFET MATLAB model:

- 1) Development of the drain-source current equation, $i_{L_{\text{fit}}}$: No equation or expression determines the drain-source current at all the MOSFET regions (subthreshold, at the threshold, above-threshold). So we modified an existing equation used in the literature [27] to fit experimental results as shown Eq. 15.
- 2) Computing the resistance of the MOSFET: After developing an equation for the current in step 1, the next step is to find the MOSFET drain-source voltage equation. To achieve this, experimental data of v_{DS} (drain-source voltage), v_{GS} (gate source voltage), and i_{L} (TMS coil current) is utilized. Curve fitting is employed on the graph of v_{DS} versus v_{GS} to obtain the $v_{\text{DS}_{\text{fit}}}$ equation. Curve fitting involves creating a mathematical function or curve that best fits the series of data points obtained from plotting v_{DS} versus v_{GS} . With the $v_{\text{DS}_{\text{fit}}}$ equation in hand, the $r_{\text{DS}_{\text{fit}}}$ of the MOSFET can be determined by:

$$r_{\text{DS}_{\text{fit}}} = \frac{v_{\text{DS}_{\text{fit}}}(v_{\text{GS}})}{i_{\text{L}_{\text{fit}}}(v_{\text{GS}})} \quad (17)$$

- 3) MATLAB model: Now we can use this $r_{\text{DS}_{\text{fit}}}$ in the differential Equation 2 as:

$$\frac{di_{\text{L}}}{dt} = \frac{1}{L} [v_{\text{C}} - (r_{\text{L}} + r_{\text{C}} + r_{\text{DS}_{\text{fit}}}(v_{\text{GS}}))i_{\text{L}}] \quad (18)$$

and solve it using any solver available in MATLAB for a given $v_{\text{GS}}(t)$ profile. We have used `ode15s` to obtain good output results depending on the input gate voltage. We have chosen the MATLAB platform for simulation instead of other options because the need for precise characterization of the MOSFET, specifically the IXFK120N65 \times 2, used in the experiment. As the circuit performance depends on the MOSFET turn-off behavior, relying solely on the on-resistance provided in the data sheet or MOSFET models in simulation software such as SPICE would not be adequate. Therefore, we have chosen to calculate the resistance of the particular MOSFET at every instant using the experimental data we have. Furthermore, there is a gap in the available literature, as there was no common drain-source current equation that could be used in all regions. Hence, we had to develop a new equation.

IV. DISCUSSION OF MODEL

We now explain the introduction of the $\ln 2$ divisor and the exponent p in Eq. (15). It is critically important to note that p is itself a function of $(v_{\text{GS}} - v_{\text{T}})$, as shown by the fitting in the next section.

A. Subthreshold

In the subthreshold region ($v_{\text{GS}} < v_{\text{T}}$), where $X \ll 1$, we set $p = 1$ (this is the maximum value for the p to capture the exponential characteristic of the subthreshold regime):

$$i_{\text{D}} = \frac{i_{\text{o}}}{\ln 2} \ln[1 + X], \quad (19)$$

and using

$$\ln[1 + X] \approx X \quad \text{for small } X$$

we obtain

$$i_{\text{D}} \approx \frac{i_{\text{o}}}{\ln 2} \exp\left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta v_{\text{Th}}}\right). \quad (20)$$

matching the Eq. (3), exponential form expected for MOSFETS.

B. Above Threshold

In the above-threshold region ($v_{\text{GS}} > v_{\text{T}}$) where $X \gg 1$, we use $p \approx 0.2$ (the value of p reduces from below-threshold to above-threshold as we need the current curve to change from exponential to linear), i_{D} becomes a linear equation:

$$i_{\text{D}} = \frac{i_{\text{o}}}{\ln 2} \ln[1 + X^p] \quad (21)$$

and using

$$\ln[1 + X^p] \approx \ln X^p \quad \text{for large } X^p$$

we obtain

$$\begin{aligned} i_{\text{D}} &= \frac{i_{\text{o}}}{\ln 2} \ln[X^p] = \frac{i_{\text{o}}}{\ln 2} \ln\left[\exp\left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta v_{\text{Th}}}\right)\right]^p \\ &= p \frac{i_{\text{o}}}{\ln 2} \left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta v_{\text{Th}}}\right) \end{aligned} \quad (22)$$

Equation (15) requires a gate-voltage dependent variable p to capture the transition from subthreshold to above-threshold behavior.

C. At Threshold

At threshold, when $v_{\text{GS}} = v_{\text{T}}$, $X^p = 1$ for any value of p

$$i_{\text{D}} = \frac{i_{\text{o}}}{\ln 2} \ln[1 + X^p] = i_{\text{o}} \quad (23)$$

The modified term ($\ln 2$) was added in Eq. (15), so that at threshold i_{D} should be equal to i_{o} .

The exponent p is defined by a tanh function of MOSFET gate voltage,

$$p = \frac{1}{2} \left(p_1 + p_2 + (p_2 - p_1) \tanh\left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta v_{\text{Th}}}\right) \right). \quad (24)$$

By modifying the Eq. 7, we achieve a successful fit that aligns with the subthreshold, at threshold, and above threshold regions. This approach treats the MOSFET as a variable resistance governed solely by the gate-source voltage, simplifying the analysis compared to treating the MOSFET as a switch. Moreover, it indirectly incorporates the influence of drain-source voltage on the resistance, thereby accounting for its effect in the model.

The crucial aspect of our application revolves around understanding MOSFET characteristics during turn-off, particularly the voltage across and current through the inductor as the MOSFET transitions to its non-conducting state. This necessitates a robust equation that accurately models behavior below, near and above threshold. Through analysis of experimental data, we derived the new equation (Eq. 15) to model high-power MOSFET behavior, thus addressing the limitations of existing analytical approaches.

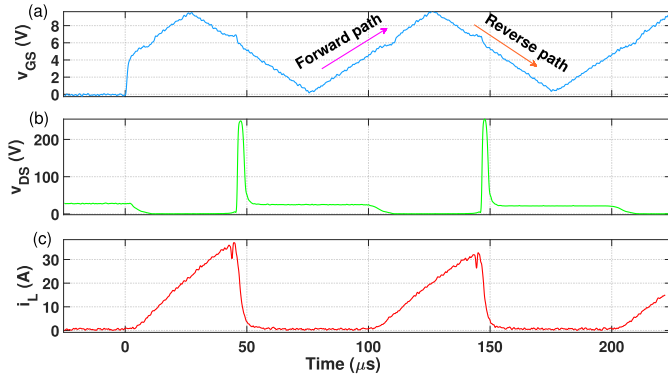


Fig. 5. (a) Gate voltage with forward path (0 to 10 V) and reverse path (10 V to 0) marked (the triangular wave is not captured for the initial 25 μs due to configuration of the triggering mechanism in the oscilloscope); (b) measured drain-source voltage and (c) coil current from the experimental setup used for the MOSFET model (To correct for current probe delay, we advanced the measured current trace by 3 μs prior to plotting).

V. COMPUTING THE RESISTANCE OF THE MOSFET

Now that the drain-source current equation that accurately fits the experimental data is developed, the next step is to determine the resistance of the MOSFET. To do so, the voltage fit equation is also required. Experimental data is utilized to understand the MOSFET behavior in the circuit, observing how the drain current and drain-source voltage vary with gate voltage. We now describe the method used to obtain both voltage and current fits, which allow us to calculate the resistance fit.

A. Voltage Fit

In our attempt to model the MOSFET S1 (Fig. 2) as a nonlinear resistance, our goal is to calculate the instantaneous dynamic resistance of the MOSFET at every instant throughout the oscillatory cycle. To do this, we measured v_{DS} and i_{D} from the experimental setup using a triangle gate voltage as input.

Figure 5 shows the experimental drain-source voltage (v_{DS}) and coil current (i_{L}) (equal to drain current i_{D}) for a triangular gate voltage. We then plotted v_{DS} against v_{GS} using the experimental data points (blue dots) as shown in Fig 6. Here, only the forward path is modeled, which was sufficient for the analysis. Additionally, the forward path varies from 0 to 10 V, which covers the full range of gate voltage used in the experiment. By analyzing the trajectory of the transfer characteristics (blue dots in Fig. 6), we found that it follows a hyperbolic tangent path since it has both upper and lower limit values. Therefore, we used the equation:

$$v_{\text{DS}_{\text{fit}}} = \frac{1}{2} \left(D_1 + D_2 + (D_2 - D_1) \tanh \left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta_1 v_{\text{TH}}} \right) \right) \quad (25)$$

The parameters used in curve fitting are provided in Table I.

B. Current Fit

From the experimental data of Fig. 5, the current versus gate voltage graph is illustrated in Fig. 8(a) with black dots, and fitted using Eq. (15). Typically, small-signal MOSFETs exhibit an η value ranging from 1 to 2. However, in this

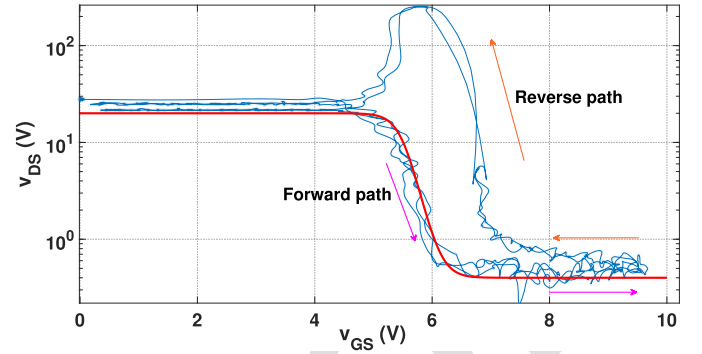


Fig. 6. Drain-source voltage versus gate voltage of the experimental result (blue). Red colored curve shows the modeled drain-source voltage fitted curve in the forward path using Eq. (25).

TABLE I

PARAMETERS AND THEIR VALUES USED FOR CURVE FITTING EQUATIONS

Measured parameters	Symbol	Values
Resistance of inductor	r_{L}	0.22 Ω
Inductance	L	0.22 μH
Internal resistance of C_1	r_{C}	0.22 Ω
Initial voltage of C_1	$v_{\text{C}}(0)$	30 V
Thermal voltage	v_{TH}	26 mV
Threshold voltage	v_{T}	6.0 V
Fitting parameters used in Equations		
$v_{\text{DS}_{\text{fit}}}$ Eq. (25)	D_1, D_2, η_1	20, 0.4, 12
i_{D} Eq. (15)	i_0, η	8, 16
p_{fit} Eq. (26)	p_1, p_2, η_2	1, 0.13, 10

analysis, we selected the η values listed in Table I to achieve a more accurate fit, with η being treated as a fitting parameter. Choosing η values greater than 1 is required to attain a better alignment with the experimental data, particularly given the specific behavior of power MOSFETs.

We use p as a function of $(v_{\text{GS}} - v_{\text{T}})$, fitted to experimental data. Specifically the exponent of Eq. (15) is given by $p = p_{\text{fit}}(v_{\text{GS}} - v_{\text{T}})$ where

$$p_{\text{fit}}(v_{\text{GS}} - v_{\text{T}}) = \frac{1}{2} \left(p_1 + p_2 + (p_2 - p_1) \tanh \left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta_2 v_{\text{TH}}} \right) \right) \quad (26)$$

The variation of p_{fit} against the gate voltage is shown in Fig. 7.

C. Resistance Fit

Once we achieve an accurate fit for drain-source voltage and drain current, we can compute the MOSFET resistance as

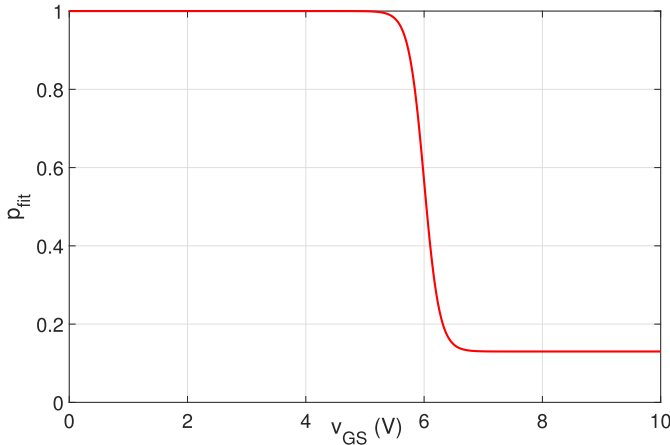


Fig. 7. Variation of exponent p_{fit} with respect to the gate-source voltage (v_{GS}).

the ratio of voltage to current:

$$r_{\text{DSfit}} = \frac{v_{\text{DSfit}}}{i_{\text{Lfit}}} \quad (27)$$

$$= \frac{\frac{1}{2} \left(D_1 + D_2 + (D_2 - D_1) \tanh \left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta_1 v_{\text{TH}}} \right) \right)}{\frac{i_0}{\ln 2} \ln \left(1 + \left[\exp \left(\frac{v_{\text{GS}} - v_{\text{T}}}{\eta v_{\text{TH}}} \right) \right]^{p_{\text{fit}}} \right)} \quad (28)$$

The MOSFET is now modeled as a resistive element whose drain-source resistance r_{DS} is a nonlinear function of v_{GS} only,

$$r_{\text{DSfit}} = f(v_{\text{GS}}) \quad (29)$$

Figure 8 shows fitting curves for current and resistance. The 3D plot in Fig. 9 illustrates the relationship between gate-source voltage, drain-source voltage, and drain current (top panel) or resistance (bottom panel).

VI. MODELING THE RLC CIRCUIT WITH TIME-VARYING MOSFET RESISTANCE- MATLAB SOLUTION

With the knowledge of the time-varying MOSFET resistance, we can model the RLC circuit using a first-order differential equation. This approach allows us to establish a dynamic MOSFET description grounded in experimental data. Such a model serves as a versatile tool that encapsulates MOSFET dynamics and allows prediction of the circuit output for arbitrary input voltage waveforms applied to MOSFET gate.

Once fitting parameters for the MOSFET voltage, current, and resistance equations have been obtained, the next step is to develop a simulation model that solves the MOSFET-controlled RLC circuit shown in Figure 2. The MOSFET is treated as a variable resistance r_{DSfit} . The circuit differential equation is:

$$\frac{di}{dt} = \frac{1}{L} \left[v_{\text{C}} - \left(r_{\text{L}} + r_{\text{C}} + r_{\text{DSfit}}(v_{\text{GS}}) \right) i \right] \quad (30)$$

where r_{L} , r_{C} , v_{C} and L are given in Table I, and $r_{\text{DSfit}}(v_{\text{GS}})$ is given in Eq. (27). Any $v_{\text{GS}}(t)$ waveform can be used to test the circuit. Here we use a triangular gate voltage generated by the `pulstran` MATLAB function to match the experimental setup shown in Fig. 5.

In this MATLAB model, Euler's method was initially used to solve the differential equations for capacitor voltage and

loop current but was later replaced by the more robust `ode15s`. Euler's method, while simple, often struggles with accuracy and stability, especially in 'stiff' systems where variables change at different rates. `ode15s`, a variable-order solver, dynamically adjusts the time step for better error control and is more suited for handling the stiffness of the equations describing the MOSFET-controlled RLC circuit. While MATLAB offers a range of ODE solvers, `ode15s` proved to be the most effective for this system, delivering accurate results that closely match the experimental values.

We have implemented the simulation to replicate oscilloscope readings, where the time base resolution determines the displayed output. Using the `ode15` solver, a resolution of $0.04 \mu\text{s}$ corresponds to a total simulation duration of 0.4 ms, while a resolution of $0.2 \mu\text{s}$ allows viewing five output cycles over 1.6 ms. We observed that the Euler solver performs effectively only at smaller time base resolutions and struggles at higher resolutions where more than two cycles need to be visualized.

The current probe used in the experiment has a bandwidth of DC-100 kHz, which leads to a slow response time. In contrast, the oscilloscope used for $v_{\text{L}}(t)$ voltage measurements, has a much higher bandwidth of 0-10 MHz. To correct for current probe delay, we advanced the measured current trace by $3 \mu\text{s}$ prior to plotting (Fig. 5). This leftwards timing offset was also applied to the current traces displayed in MATLAB plots of Figures 10(a) and 11(a). Figure 10(a) shows a good match between experimental measurements and MATLAB model predictions for a triangular gate input voltage. Figure 10(b) shows the oscilloscope capture results of the same 50% symmetry gate voltage input used in simulation.

MATLAB simulations produce a perfect gate waveform without any distortion. However, in the experimental setup, the gate waveform exhibits minor distortions caused by the 8-bit quantization noise in digital oscilloscope captures. In Figure 10, the leading edge of the gate voltage shows a step discontinuity instead of the expected smooth voltage ramp. This discrepancy arises because the initial $25 \mu\text{s}$ of the triangular wave is not captured accurately due to the oscilloscope triggering configuration. However, focusing on the second pulse, we observe a good alignment between the simulation results and the circuit measurements. Since we have not modeled the charging and discharging of the gate capacitance, this process causes a small blips (black circle) at the threshold voltage of the gate voltage, as shown in Fig. 10. These small imperfections can affect the current and voltage waveforms, making them slightly less precise compared to the ideal simulations. Despite these minor differences, the experimental waveforms match the MATLAB-generated output, confirming that the curve-fitting equations have successfully captured the nonlinear resistance behavior of the MOSFET.

VII. VALIDATION

As a test of the simulation equations, we replaced the triangular gate voltage with a 10 kHz 10 V_{pp} sinusoidal signal, but modeled with the same parameters as the triangle waveform we compared the output from the `ode15s` solver

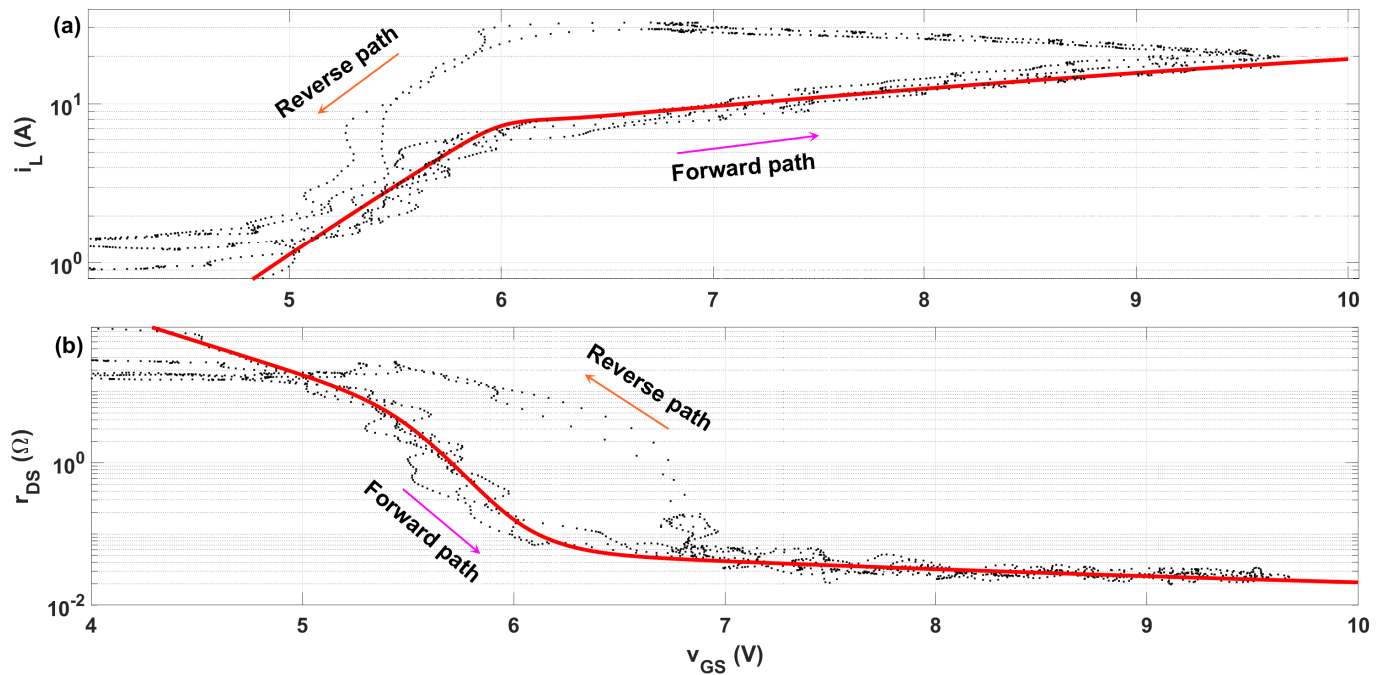


Fig. 8. (a) Transfer characteristics with fitted current curve (red line) and measured data (black dots) using Eq. (15); (b) drain-source resistance with fitted drain-source resistance curve using Eq. (27).

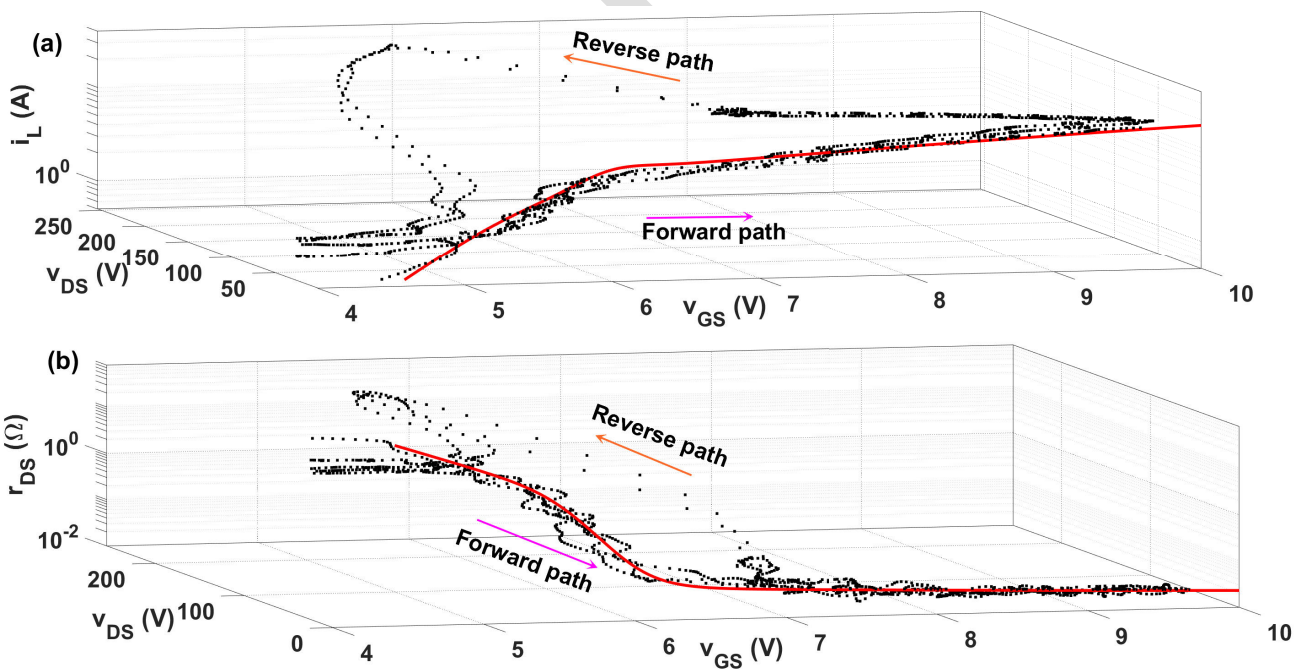


Fig. 9. Three dimensional view of the output characteristic of the MOSFET and dynamic resistance curve; (a) The measured output characteristic is shown in black dots, and the red curve shows the fitted drain-source voltage in the forward path; (b) black dots show the dynamic characteristic and, the red curve shows the fitted drain-source resistance.

539 with experimental measurements. Figure 11(a) superimposes
 540 the simulation predictions against experimental measurements
 541 from the oscilloscope capture shown in Fig. 11(b). It is
 542 interesting to observe that the output current and voltage
 543 waveforms are surprisingly insensitive to the shape (triangular
 544 or sinusoidal) of the input gate voltage drive signal. This is
 545 because the high voltage occurs near the threshold region

primarily during turn-off, as the inductor coil only stores
 energy at that point. In both sinusoidal and triangular
 waveforms, the slope/points near the turn-off point around
 the threshold region is similar, resulting in comparable output
 behavior.

Figure 12 presents the experimental and MATLAB results
 for the quadrilateral gate voltage waveform. The results

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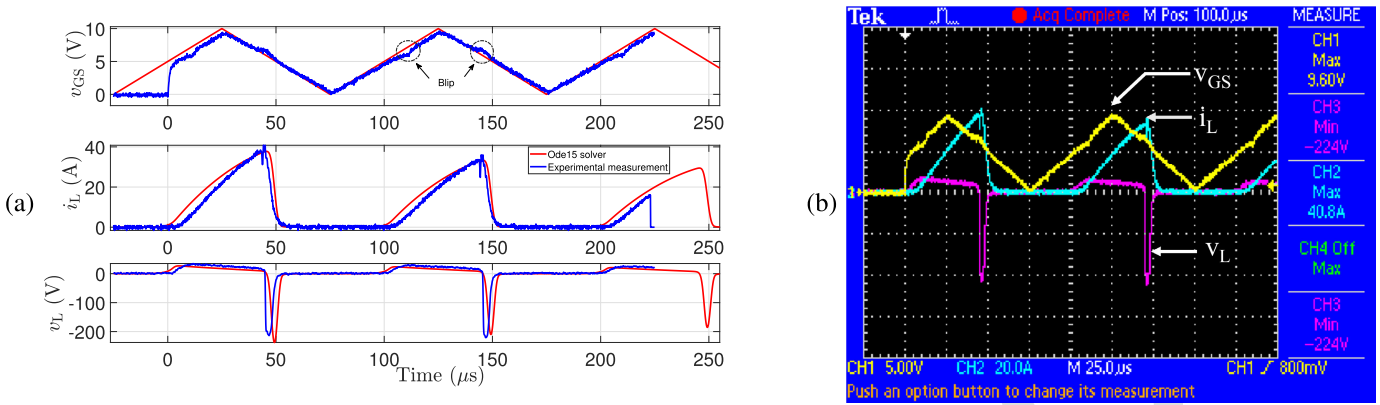


Fig. 10. Comparison of `ode15s` solver solution of the differential equation with the measured experimental values. The experimental measurement is the MATLAB of the oscilloscope view shown in (b). Blips due to charging and discharging of gate capacitance are highlighted (black circles). Note that the triangular wave is not accurately captured for the initial $25 \mu\text{s}$ due to the configuration of the triggering mechanism in the oscilloscope; (b) oscilloscope measurement of gate voltage (yellow), output coil voltage (magenta) and coil current (blue).

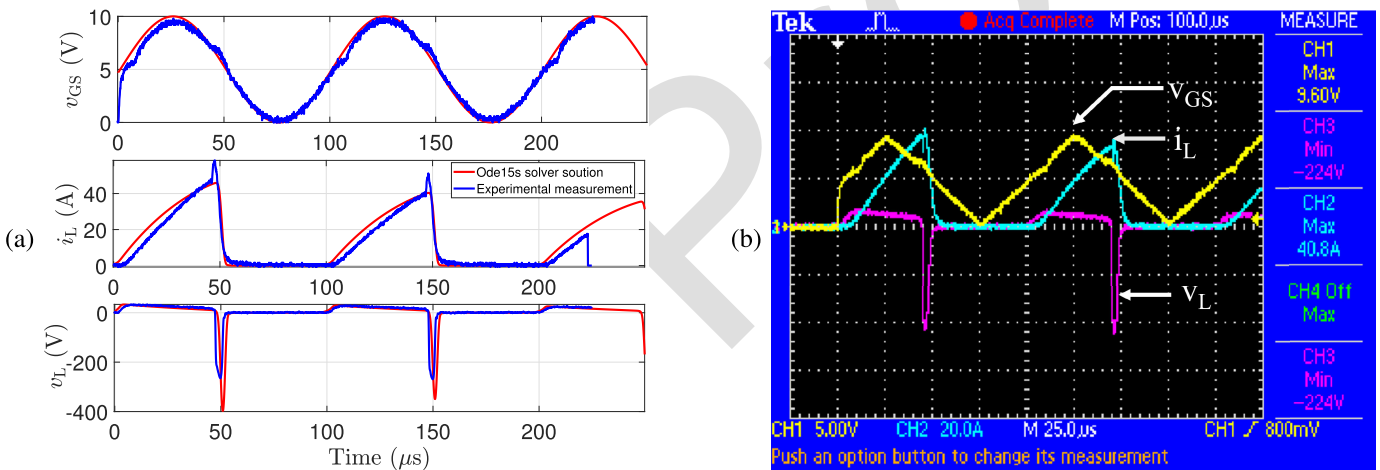


Fig. 11. (a) `ode15s` Solver solution of the differential equation (red) and experimental results (blue) with sinusoidal gate voltage input; (b) Oscilloscope measurement of sinusoidal gate voltage (yellow), output coil voltage (magenta) and coil current (blue).

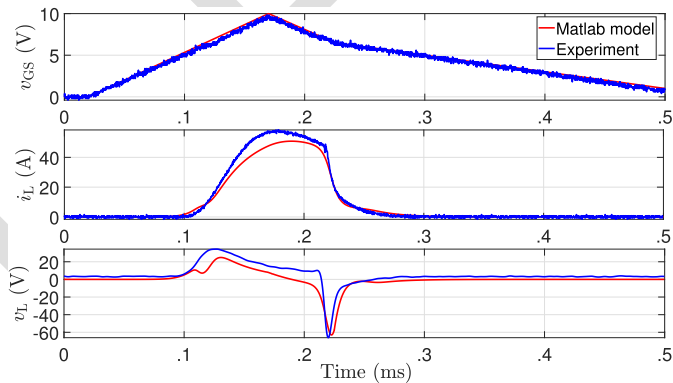


Fig. 12. Verification of experimental and MATLAB data with a quadrilateral gate voltage of 10 V amplitude.

553 indicate a good match between the TMS current and voltage
554 pulses observed in the experimental setup and those from the
555 MATLAB simulations.

556 VIII. DISCUSSION

557 This approach to MOSFET modeling offers two key advan-
558 tages. Firstly, by incorporating a comprehensive mathematical

559 model of MOSFET behavior, simulation results provide a good
560 match to the real-world scenarios. Secondly, the flexibility
561 of the code allows for easy modification and adaptation
562 to different MOSFET specifications or circuit configurations.
563 Overall, this code segment serves as a robust tool for analyzing
564 MOSFETs-driven RLC circuits, enabling researchers to gain
565 insights into circuit performance and behavior under varying
566 conditions.

567 However, our approach is carried out in the context of an
568 RLC circuit. Use of Equations (27)-(28) implicitly assumes
569 particular voltage v_{DS} dynamics, and would likely need
570 modifications for a different context. Here we have curve fitted
571 with the parameter range that we got from the experimental
572 data and also by looking the pattern. In contrast, variation
573 of i_{DS} with v_{GS} near threshold is likely to be more robust,
574 approximately independent of v_{DS} as shown in Fig. 3(b).
575 Although we have not yet performed reverse path-curve fitting,
576 Figures 8 and 9 shows a hysteresis separation from the forward
577 path; we believe it is closely related to the gate capacitance
578 charge and discharge dynamics [30]. Despite focusing solely
579 on the forward path from 0 to 10 V, the model still offers a
580 reliable prediction of the output compared to the experimental
581 results. This accuracy can be attributed to the similarity in

shape between the forward and reverse paths in the resistance plot shown in Figure 8, despite a time shift. Consequently, even without explicitly modeling the reverse path, the negative phase of the voltage in the simulation aligns closely with the experimental results.

During experimental data collection, several measurement challenges were encountered. The oscilloscope resolution made it difficult to capture the full range of data with the maximum number of points for both high and low voltage levels. When using a 100X probe, the minimum voltage values lacked accuracy. To address this, two probes were employed: a 100X probe for higher voltage levels and a 10X probe for lower voltage levels. The data from both probes were then combined in the MATLAB model.

Additionally, the resolution of the current probe and the oscilloscope differed, and we were unable to calibrate the zero-current setting on the probe. As a result, the initial inductor current values (i_L) were not zero or very small, as expected. These imperfections will be addressed in the next stage of the model, where the gate capacitance will be considered. The energy dissipated and power delivered during the tests were lower than the rated values specified in the datasheet. Additionally, the MOSFET did not heat up during testing, indicating minimal thermal stress. As a result, the temperature effect has not been considered in the current analysis.

Existing studies on SiC MOSFET modeling, such as those by Tornello [9], Ferretti [10], and Wang [12], emphasize datasheet-based methods, lookup tables, and linear analyses without considering switching transients. However, they often overlook internal variations like dynamic resistance changes. Our approach addresses this gap by modeling the MOSFET as a dynamically varying resistor, considering smoothing phase transition between the sub threshold and above threshold region rather than piecewise analysis offering deeper insights into device behavior and predicting the output.

IX. CONCLUSION

The development of the analytical model for nonlinear MOSFET dynamics faced several challenges, including limited data from datasheets, unexpected experimental observations, and measurement difficulties. Despite these hurdles, the model was successfully refined to accurately predict system behavior. The neglect of gate capacitance effects, although simplifying the model, did not significantly impact the accuracy of the results, which closely matched experimental data. The complexity of modeling MOSFET dynamics in the RLC circuit became evident, particularly when simulation tools like Proteus failed to replicate real-world results. However, by fine-tuning resistance, threshold values, and utilizing curve fitting based on a physical descriptions of the near threshold region, the MATLAB model provided reliable predictions. This work offers a strong physical foundation for power MOSFET modeling, allowing for more accurate simulations with minimal additional experimentation.

This study can be significant because, in the existing literature, there are primarily Simulink block models or subthreshold equations, with no dedicated equation available to cover the full operating range of a MOSFET. Our approach

can serve as a valuable tool for other researchers to evaluate circuits where the MOSFET is treated as a dynamically varying element.

X. FUTURE WORK

In future work, we are working to incorporate stray inductance and capacitance effects into the model, as these factors play a critical role in circuit performance. While this study focuses on the initial development stage, we recognize the importance of addressing these elements and will include them in the next phase.

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799 Waikato in 2021. For developing supercapacitor-assisted (SCA) techniques,
800 he won the NZ Engineering Innovator of the Year 2013 Award.